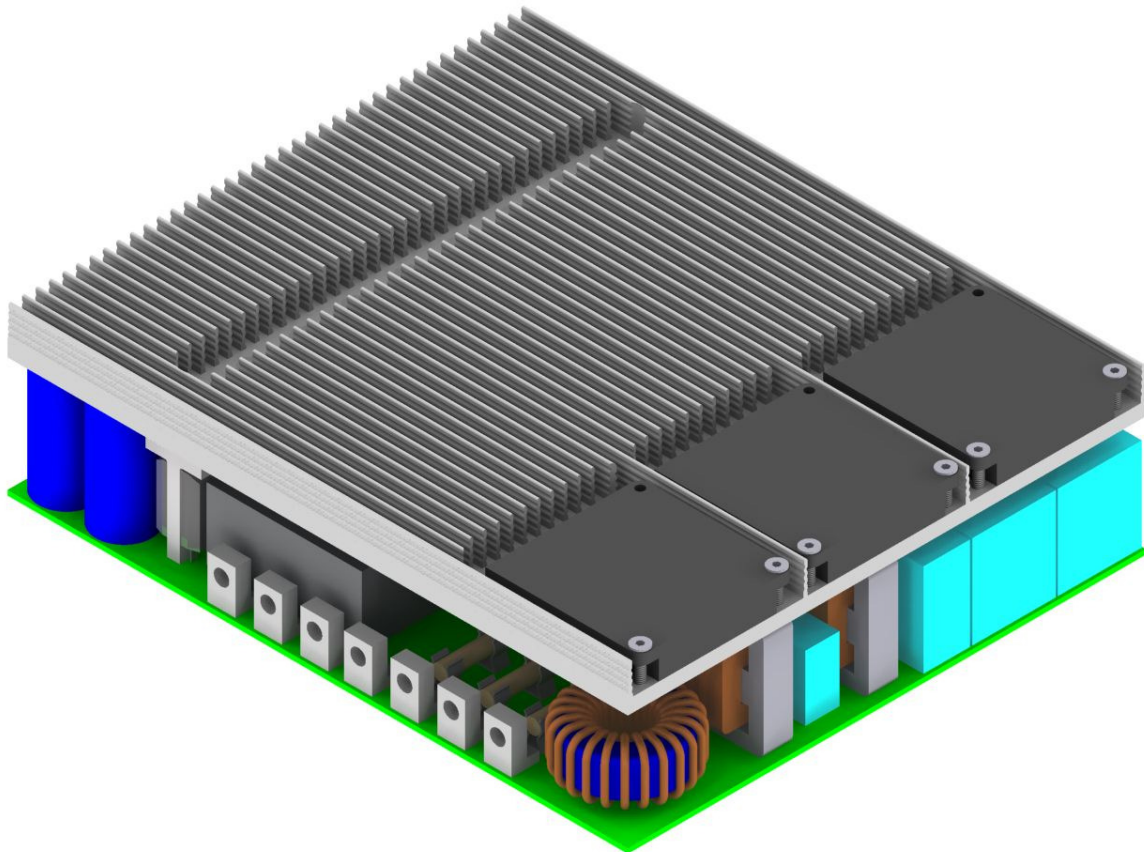


Datasheet 16.11.2011

# VIENNA Rectifier Technology Package "VR73"



Johann Miniböck  
**m-pec**  
power electronics consulting

m-pec  
Johann Miniböck  
power electronics consulting  
- design and prototyping -  
Purgstall 5  
3752 Walkenstein  
Austria  
miniboeck@mpec.at  
www.mpec.at  
+43-2913-411

 **IPEC**

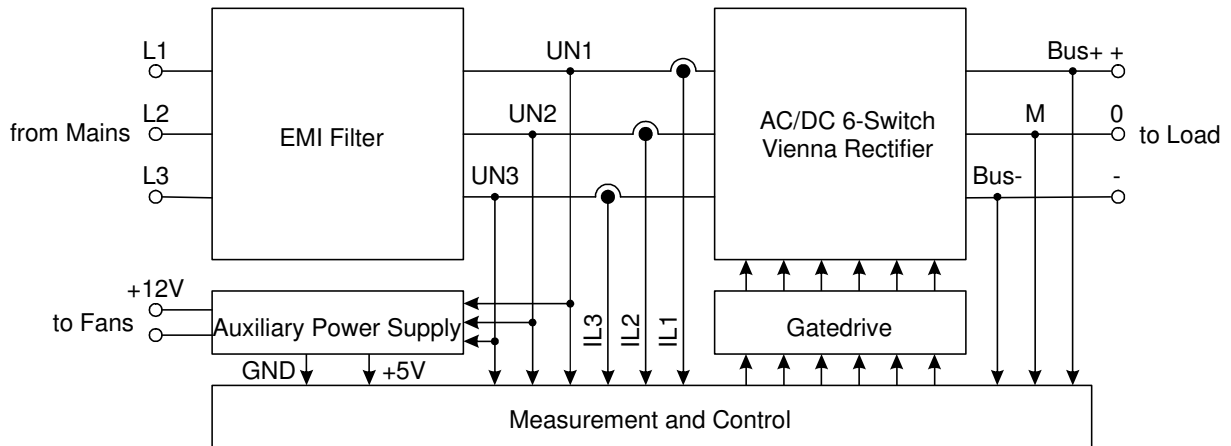
IPEC GmbH / LLC  
International Power Electronics Consulting  
Zürichbergstrasse 124  
8044 Zürich  
Switzerland

**Table of Contents**

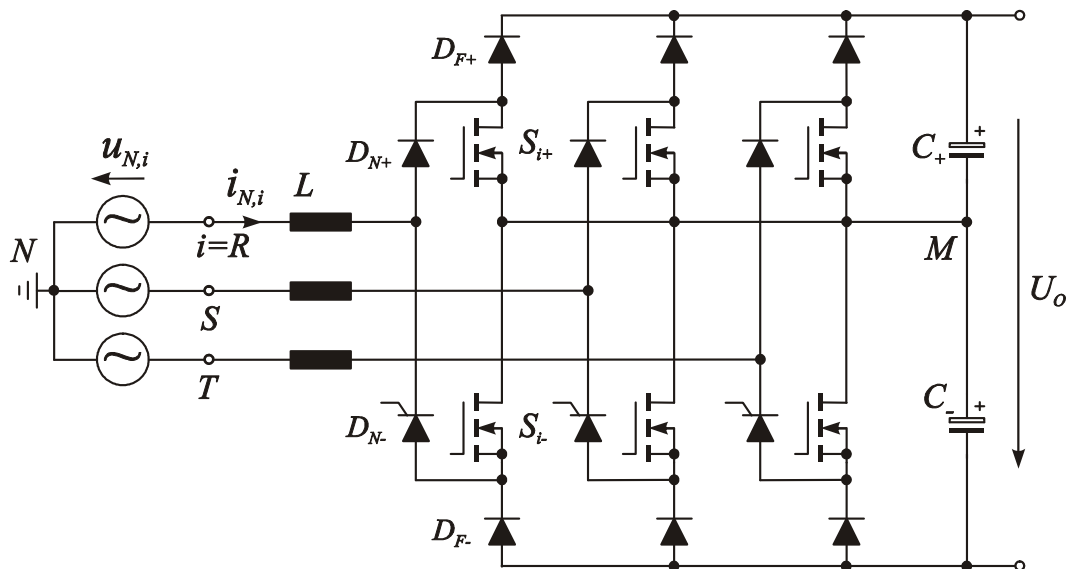
- 1 Introduction..... 3**
  - 1.1 Circuit Topology ..... 3
  - 1.2 Specification ..... 5
    - 1.2.1 General ..... 5
    - 1.2.2 Input..... 5
    - 1.2.3 Output..... 5
- 2 Literature..... 6**

# 1 Introduction

## 1.1 Circuit Topology



**Fig.1.1:** Block diagram of the VR73 Evaluation System.



**Fig.1.2:** Structure of the power circuit of the realized 6-switch PWM (VIENNA) rectifier system.

The basic structure of the three-phase three-level unity power factor rectifier is shown in Fig.1.2. The system is of equal functionality like the VIENNA Rectifier ([1], [2], [3]), the advantage of the six-switch topology is the reduced conduction loss, because of the absence of the center point diodes. The

technical and economic advantages of the system as compared to alternative rectifier concepts were analyzed in detail in [6] and [7]. The main advantages are [11]:

- simple analogue or digital control circuit
- three-level characteristic of the bridge legs resulting in
- low voltage stress on the power semiconductors
- low amplitudes of switching frequency harmonics of the input phase currents
- two controlled partial output voltages and/or high current handling capability of the output voltage center point in case of asymmetric distribution of the load to the partial voltages [8]
- high reliability (no possibility of a short-circuit on the output voltage in case of control errors).

## 1.2 Specification

Full digital control by TI DSP

### 1.2.1 General

Control structure	<ul style="list-style-type: none"> <li>- Output voltage control</li> <li>- Output voltage symmetry control</li> <li>- Average current mode input current control</li> <li>- 3rd harmonic injection</li> <li>- Input voltage feed-forward</li> <li>- Continuous operation at mains voltage unbalance with reduced output power</li> </ul>
Protection functions (by disabling all switching signals, unit recovers by reset)	<ul style="list-style-type: none"> <li>- Input overcurrent protection</li> <li>- Output overvoltage</li> <li>- Input voltage out of limits</li> </ul> <p>(Unit recovers automatically if voltage returns in specified range)</p>
Indications	green, yellow and red status LEDs
Dimensions (l x w x h)	200 x 167 x 58mm <sup>3</sup>
Power density (aircooled)	6.2kW/dm <sup>3</sup>
Switching frequency	70kHz

### 1.2.2 Input

Input line to line voltage range nominal	400V...480VAC AC, 50...60Hz
Input line to line voltage limits	320V...530VAC
Input current max	22.5A rms
Power factor (> 25% load, 50Hz)	> 0.99
THD <sub>I</sub> of input currents	< 3%
	(THD <sub>U</sub> < 1%, 100% load)
Maximum input current unbalance	± 10% (symmetric mains)

### 1.2.3 Output

Rated output power	12kW
Rated output voltage	800VDC (± 400VDC)
Reduced output power for two phase operation	57 % $P_{O,nom}$
Overvoltage protection	± 450VDC

## 2 Literature

- [1] **Kolar, J.W.:** *Dreiphasen-Dreipunkt-Pulsgleichrichter*. Österreichische Patentanmeldung, Aktenzeichen: A2612/93. Das Patent wurde 1994 von ABB IXYS Semiconductor GmbH übernommen und unter dem Titel *Vorrichtung und Verfahren zur Umformung von Drehstrom in Gleichstrom* als Europapatent angemeldet.
- [2] **Kolar, J.W., and Zach, F.C.:** *A Novel Three-Phase Three-Switch Three-Level PWM Rectifier*. Proceedings of the 28<sup>th</sup> Power Conversion Conference, Nürnberg, June 28 – 30, pp. 125 – 138 (1994).
- [3] **Kolar, J.W., and Zach, F.C.:** *A Novel Three-Phase Utility Interface Minimizing Line Current Harmonics of High-Power Telecommunications Rectifier Modules*. Proceedings of the 16<sup>th</sup> IEEE International Telecommunications Energy Conference, Vancouver, Oct. 30 – Nov 3, pp. 367 – 374 (1994).
- [4] **Kolar, J.W.:** *VIENNA Rectifier – Entwicklung und Analyse neuer netzrückwirkungsarmer Dreiphasen-Pulsgleichrichtersysteme*. Dissertation, Wien, June 1998.
- [5] **Drofenik, U.:** *Optimierung und experimentelle Analyse des stationären Betriebsverhaltens eines VIENNA Rectifier I*. Dissertation, Wien, Nov. 1998.
- [6] **Ide, P., Froehleke, N., and Grotstollen, H.:** *Comparison of Selected 3-Phase Switched-Mode Rectifiers*. Proceedings of the 19th International Telecommunications Energy Conference, Melbourne, Australia, S. 630 – 636 (1997).
- [7] **Kolar, J.W., and Ertl, H.:** *Status of Techniques of Three-Phase Rectifier Systems with Low Effects on the Mains*. Proceedings of the 21st IEEE Telecommunications Energy Conference, Copenhagen, Denmark, June 6 – 9, paper no.: 14-1 (1999).
- [8] **Kolar, J.W., Drofenik, U., and Zach, F.C.:** *Current Handling Capability of the Neutral Point of a Three-Phase/Switch/Level Boost-Type PWM (VIENNA) Rectifier*. Proceedings of the 28th IEEE Power Electronics Specialists Conference, Baveno, Italy June 24 – 27, Vol. II, pp. 1329 – 1336 (1996).

- [9] **Zhao, Y., Li, Y., and Lipo, T.A.:** *Force Commutated Three-Level Boost Type Rectifier*. Record of the 28th IEEE Industry Applications Society Annual Meeting, Toronto, Canada, Oct. 2 – 8, Vol. II, pp. 771 – 777 (1993).
- [10] **Kolar, J.W., Ertl, H., and Zach, F. C.:** *Design and Experimental Investigation of a Three-Phase High Power Density High Efficiency Unity Power Factor PWM (VIENNA) Rectifier Employing a Novel Power Semiconductor Module*. Proceeding of the 11<sup>th</sup> IEEE Applied Power Electronics Conference, San Jose, USA, Mar. 3 – 7, Vol. 2, pp. 514 – 523 (1996).
- [11] **Kolar, J.W., Drofenik, U., Miniböck, J., and Ertl, H.:** *A New Concept for Minimizing High-Frequency Common-Mode EMI of Three-Phase PWM Rectifier Systems Keeping High Utilization of the Output Voltage*. Record of the 15th IEEE Applied Power Electronics Conference, New Orleans, Feb 6 – 10, Vol. 1, pp. 519 – 527 (2000).
- [12] **Miniböck, J., Stögerer, F., and Kolar, J.W.:** *A Novel Concept for Mains Voltage Proportional Input Current Shaping of a VIENNA Rectifier Eliminating Controller Multipliers. Part I: Basic Theoretical Considerations and Experimental Verification*. Proceedings of the 16th IEEE Applied Power Electronics Conference, Anaheim, March 4 – 8, (2001).
- [13] **Stögerer, F., Miniböck, J., and Kolar, J.W.:** *A Novel Concept for Mains Voltage Proportional Input Current Shaping of a VIENNA Rectifier Eliminating Controller Multipliers. Part II: Operation for Heavily Unbalanced Mains Phase Voltages and in Wide Input Voltage Range*. Proceedings of the 16th IEEE Applied Power Electronics Conference, Anaheim, March 4-8, (2001).
- [14] **Miniböck, J., Stögerer, F., and Kolar, J.W.:** *Comparative Theoretical and Experimental Evaluation of Bridge Leg Topologies of a Three-Phase Three-Level Unity Power Factor Rectifier*. Proceedings of the IEEE Power Electronics Specialists Conference, Vancouver, Canada, June 17 – 21, 2001.
- [15] **Shonts, D.:** *Improved PFC Boost Choke using a Quasi-Planar Winding Configuration*. Proceeding of the 14th IEEE Applied Power Electronics Conference, Dallas (TX), USA, March 14 - 18 (1999).